

EAST [09513634.wsp II] X

File View Edit Tools Window Help

Drafts []

- BRS: warp and 1 and 2
- BRS: 6 and
- BRS:
- BRS: 11 and
- Pending

Active []

- L0: (04676) diamond (ni nickel) adj5 diamond garnet (silicon adj carbide) sic1
- L0: (17776) 1 with particle
- L0: (354) 1 with (hard and particle)
- L0: (8) 3 and (flip with chip)
- L0: (880) 3 and (semiconductor chip die in (integrated adi circuit))
- L0: (44) (pb board pcb cb (printed adj3 board) substrate) and 1
- L0: (64) plated adj5 (plated adj4 (ni nickel))
- L0: (1) 6 and 7
- L0: (11712) (plated adj5 (ni nickel))
- L0: (14) 6 and 9
- L0: (88) (pb board pcb cb (printed adj3 board)) and 5
- L0: (1) 11 and 6

Failed []

- (1) (easy electric resistance at nickelized adj5 (silicone rubber ethylene carbon plastic) thermal)
- (1) 4 same 5
- (1) 14 with 1
- (1) (dummy with (semiconductor chip die in (integrated adi circuit)) and 16

Caved []

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CCD []

Others []

Search Id Deleted Queue Clear []

DB: USPAT, US PGPUB, EPO, Elink Synchrony

Default operator: OR Highlight of terms only

11 and []

Actions []

Document ID Issue Date Pages Title Current OR Current XRef Retrieval C Inventor S C P R 3 []

Ready

EAST [0932986.wsp 1]									
File New Edit View Window Bar									
Search									
Patent									
<input checked="" type="checkbox"/> Active									
<input checked="" type="checkbox"/> L1	Flexible board w/ particulate diamond (imbedded in platen) and diamond/garnet (silicon carbide)								
<input checked="" type="checkbox"/> L2	(005150) semiconductor chip die dice in (integrated adj circuit)								
<input checked="" type="checkbox"/> L3	004 40 1 and 2 and substrate carrier plate w/ printed circuit adj or heat								
<input checked="" type="checkbox"/> L4	002110 1 and 2 same substrate board pos it on printed circuit board adj								
<input checked="" type="checkbox"/> L5	001 4 semicrystalline high temp walls								
<input checked="" type="checkbox"/> L6	0010 1 same (semicrystalline high temp walls)								
<input checked="" type="checkbox"/> L7	001 1 and 2 steel high temp walls								
<input checked="" type="checkbox"/> L8	002110 (based adj particulate diamond (imbedded in platen) and diamond/garnet (silicon carbide))								
<input checked="" type="checkbox"/> L9	001 4 semicrystalline high temp walls								
<input checked="" type="checkbox"/> Failed									
<input checked="" type="checkbox"/> F1	001 1 and 2 and 3								
<input checked="" type="checkbox"/> Pass									
<input checked="" type="checkbox"/> P1	006305 bendable flexible membrane with frame container supported								
<input checked="" type="checkbox"/> P2	0010 1 of the 1 or 2 integrated adj circuit in same membrane bendable flexible membrane supported with frame								
<input checked="" type="checkbox"/> P3	0010 1 bendable flexible membrane with frame container supported								
<input checked="" type="checkbox"/> P4	006305 bendable flexible membrane with frame container supported same integrated adj circuit in same membrane bendable flexible membrane with frame								
<input checked="" type="checkbox"/> P5	0010 1 of the 1 or 2 integrated adj circuit in same bendable flexible membrane with frame								
<input checked="" type="checkbox"/> P6	0030 1 bendable flexible membrane with frame container supported								
<input checked="" type="checkbox"/> P7	0030 1 bendable flexible membrane with strong container supporters and 1 chip die dice in 1								
<input checked="" type="checkbox"/> P8	0030 1 bendable flexible membrane with frame container supporters and 1 chip die dice in 1								
<input checked="" type="checkbox"/> P9	0011 40 1 hard and particulate diamond (imbedded in platen) and diamond/garnet (silicon carbide)								
<input checked="" type="checkbox"/> P10	(005150) semiconductor chip die dice in (integrated adj circuit)								
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